

AMENDMENTS TO THE ABSTRACT:

Please amend the Abstract as follows:

The present invention provides a technique for synchronisation between pipelines. Pipelines are synchronised in a data processing apparatus that. The data processing apparatus comprises includes a main processor operable to execute for executing a sequence of instructions, the main processor comprising a first pipeline having a first plurality of pipeline stages, and a coprocessor operable to execute for executing coprocessor instructions in said that sequence of instructions. The coprocessor comprises a second pipeline having a second plurality of pipeline stages, and each Each coprocessor instruction is arranged to be routed through both the first and second pipeline and the second pipeline pipelines. Furthermore, at least one A synchronising queue is provided coupling couples a predetermined pipeline stage in one of the pipelines with a partner pipeline stage in the other of the pipelines., the The predetermined pipeline stage being operable to cause causes a token to be placed in the synchronising queue when processing a coprocessor instruction, and the The partner pipeline stage being operable to process processes that coprocessor instruction upon receipt of the token from the synchronising queue. By this approach, the first and second pipelines are synchronised between the predetermined pipeline stage and the partner pipeline stage, and hence This ensures that the pipelines are correctly synchronised for crucial transfers of information without requiring that strict synchronisation at all stages is necessary.